



Substitute for form 1449A/PTO
**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Use as many sheets as necessary)

Complete if Known

Application Number	09/239,898
Filing Date	January 29, 1999
First Named Inventor	Seyyedy, Mirmajid
Group Art Unit	2133
Examiner Name	Lamarre, Guy

Sheet 1 of 1

Attorney Docket No: 303.550US1

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. 1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T
		"16Mb Double Data Rate Synchronous Graphics RAM", IBM Corporation, IBM0616328RL6A, (December 1997), pp. 1-53	
		"16Mbit DDR SGRAM, 128K x 32Bit x 4 Banks Double Data Rate Synchronous Graphic RAM with Bi-directional Data Strobe, Revision 0.6", Samsung Electronics, KM432D5131, (April 1998), pp. 1-48	

EXAMINER

DATE CONSIDERED 4/22/05